

[0038] **FIG. 14B** is an exploded perspective view of a portion of an enclosure for an electronic device according to another embodiment of this invention.

[0039] **FIG. 14C** is an exploded perspective view of a portion of an enclosure for an electronic device according to yet another embodiment of this invention.

[0040] **FIG. 15A** is a plan view of a sheet including a plurality of battery cells.

[0041] **FIG. 15B** is a plan view of a diced battery cell before forming.

[0042] **FIG. 15C** is a perspective view of a battery cell after forming.

[0043] **FIG. 15D** is a perspective view of a battery cell after forming.

[0044] **FIG. 15E** is a perspective view of a battery cell after forming.

[0045] **FIG. 15F** is a plan view of a sheet including a plurality of battery cells.

[0046] **FIG. 15G** is a plan view of a plurality of diced battery cells before forming.

[0047] **FIG. 15H** is a perspective view of a fan folded plurality of diced battery cells before forming.

[0048] **FIG. 15I** is a plan view of a sheet including a plurality of battery cells.

[0049] **FIG. 15J** is a perspective view of a sheet including a plurality of battery cells formed on the sheet according to this invention.

[0050] **FIG. 15K** is a perspective view of a sheet including two battery cells formed into a case from the sheet shown in **FIG. 15J** according to this invention.

[0051] **FIG. 15L** is a side view of an electronic device enclosure formed from a sheet.

[0052] **FIG. 16A** is a plan view of a sheet including a plurality of battery cells.

[0053] **FIG. 16B** is a plan view of a diced battery cell before forming.

[0054] **FIG. 16C** is a perspective view of a fan folded diced battery cell before forming.

[0055] **FIG. 16D** shows a fan folded cord being truncated.

[0056] **FIG. 16E** shows a finished cord.

[0057] **FIG. 17** is an exploded perspective view of a sheet including at least one battery cell rolled around an electrical motor in accordance with this invention.

[0058] **FIG. 18A** is a plan view of a diced battery cell and LED before forming.

[0059] **FIG. 18B** is a perspective view of a diced battery cell and LED after forming.

[0060] **FIG. 18C** is a plan view of a diced battery cell and LED before forming.

[0061] **FIG. 18D** is a perspective view of a diced battery cell and LED after forming.

[0062] **FIG. 19A** is a plan view of a sheet including a plurality of battery cells according to another embodiment of this invention.

[0063] **FIG. 19B** is a plan view of a plurality of diced battery cells before forming.

[0064] **FIG. 19C** is a perspective view of a formed battery including a plurality of cells.

[0065] **FIG. 20** is a cutaway side view of a sheet including a plurality of battery cells, which are embedded in an enclosure portion.

[0066] **FIG. 21A** is a flow chart for a first recycling method using the inventive battery and enclosure.

[0067] **FIG. 21B** is a flow chart for a first recycling method using the inventive battery and enclosure.

[0068] **FIG. 22A** shows a schematic circuit of an embodiment of an integrated battery and circuit sharing a common terminal.

[0069] **FIG. 22B** shows a block diagram perspective view of an integrated device implementing the circuit of **FIG. 22A** having the circuit built on the battery.

[0070] **FIG. 22C** shows a block diagram perspective view of an integrated device implementing the circuit of **FIG. 22A** having the battery built on the circuit.

[0071] **FIG. 22D** shows a schematic circuit of an embodiment **2202** of an integrated battery and circuit each having separate terminals.

[0072] **FIG. 22E** shows a block diagram perspective view of an integrated device implementing the circuit of **FIG. 22D** having the circuit built on the battery.

[0073] **FIG. 22F** shows a block diagram perspective view of an integrated device implementing the circuit of **FIG. 22D** having the battery built on the circuit.

[0074] **FIG. 22G** shows a block diagram perspective view of an integrated device implementing the circuit of **FIG. 22A** having the battery and the circuit built side-by-side on a substrate.

[0075] **FIG. 22H** shows a block diagram perspective view of an integrated device implementing the circuit of **FIG. 22D** having the battery and the circuit built side-by-side on a substrate.

[0076] **FIG. 23** shows a perspective view of an embodiment **2300** of the present invention having a battery overlaid with circuitry.

[0077] **FIG. 24A** shows a perspective view of an embodiment **2400** of the present invention having a battery overlaid with an integrated device.

[0078] **FIG. 24B** shows a block diagram of a layer-deposition system **2460**.

[0079] **FIG. 24C** shows a perspective view of a partially processed sheet **2464**.

[0080] **FIG. 24D** shows a block diagram of a layer-deposition system **2465**.

[0081] **FIG. 24E** shows a perspective view of a processed sheet **2469**.